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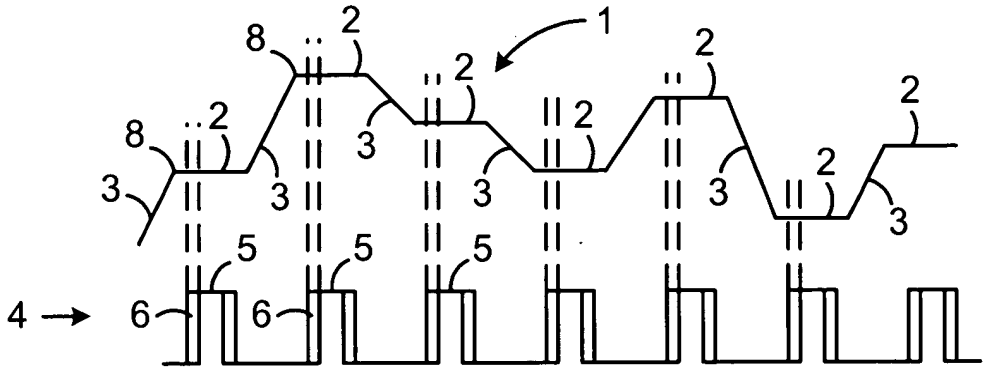

FIG. 1

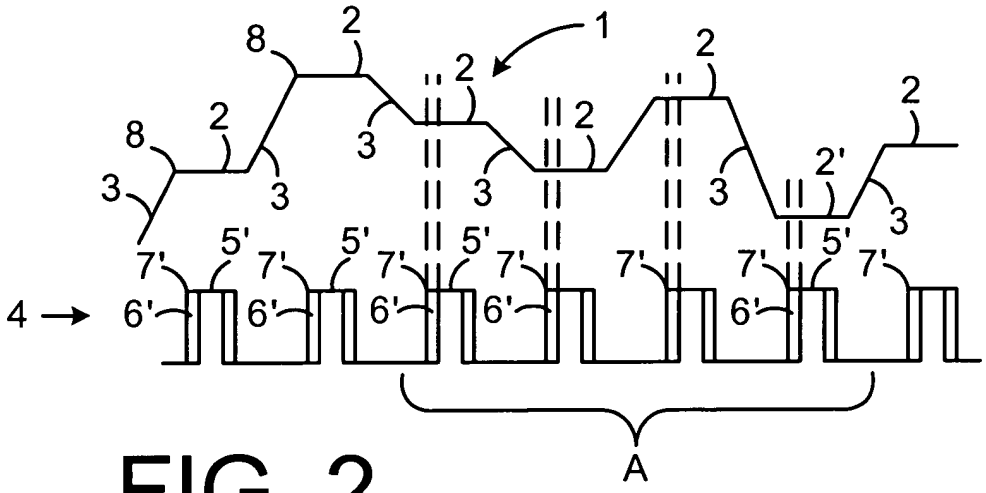

FIG. 2

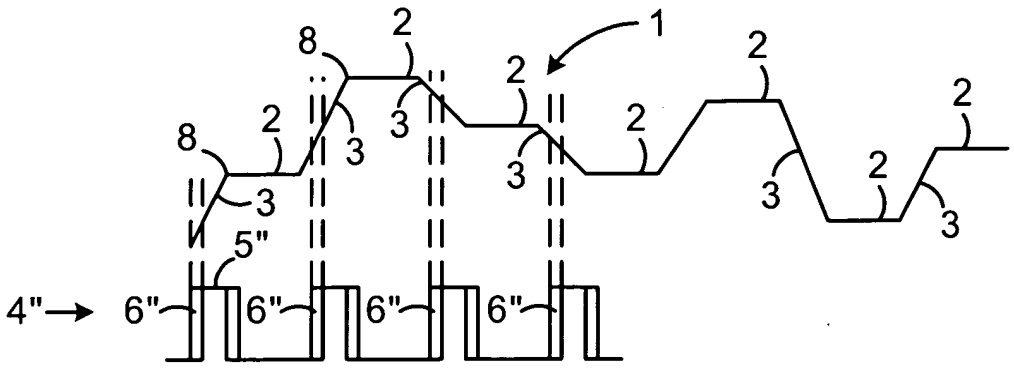

FIG. 3

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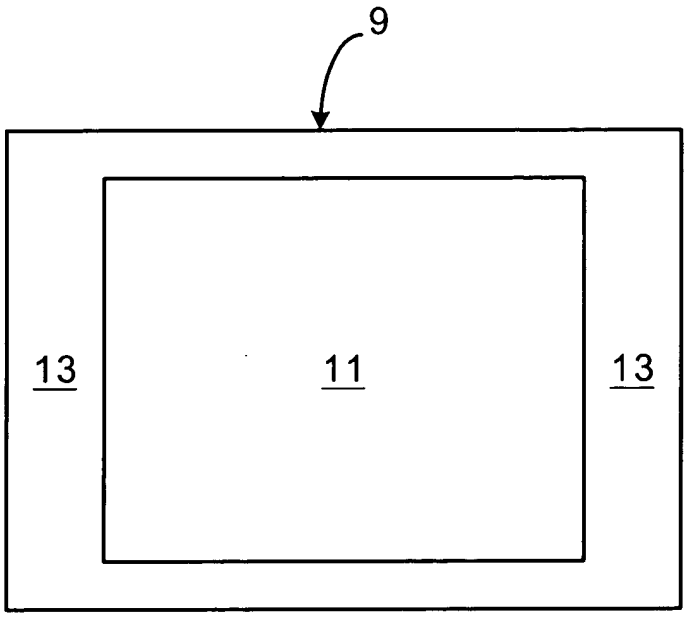

FIG. 4

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JC184

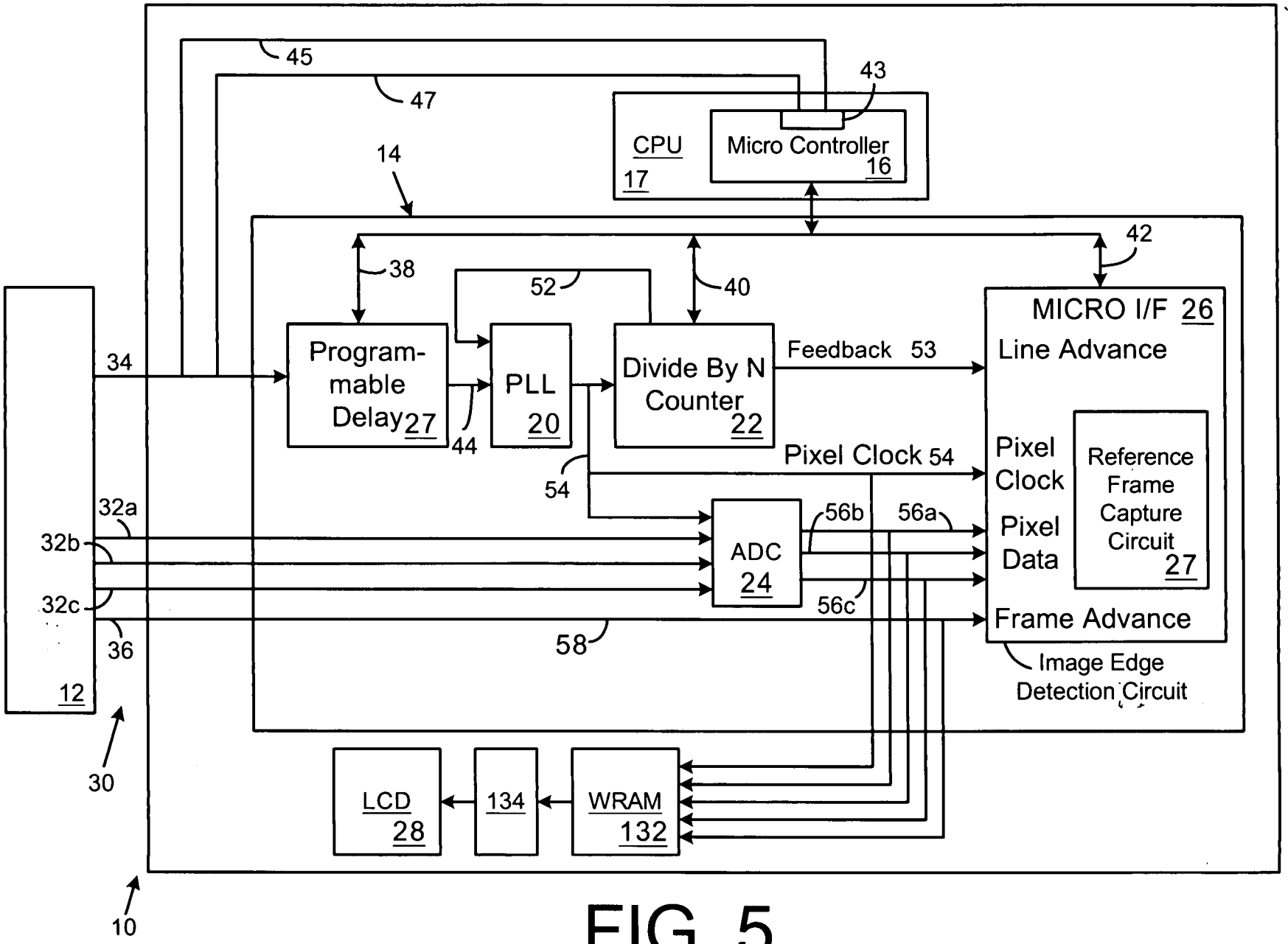

FIG. 5

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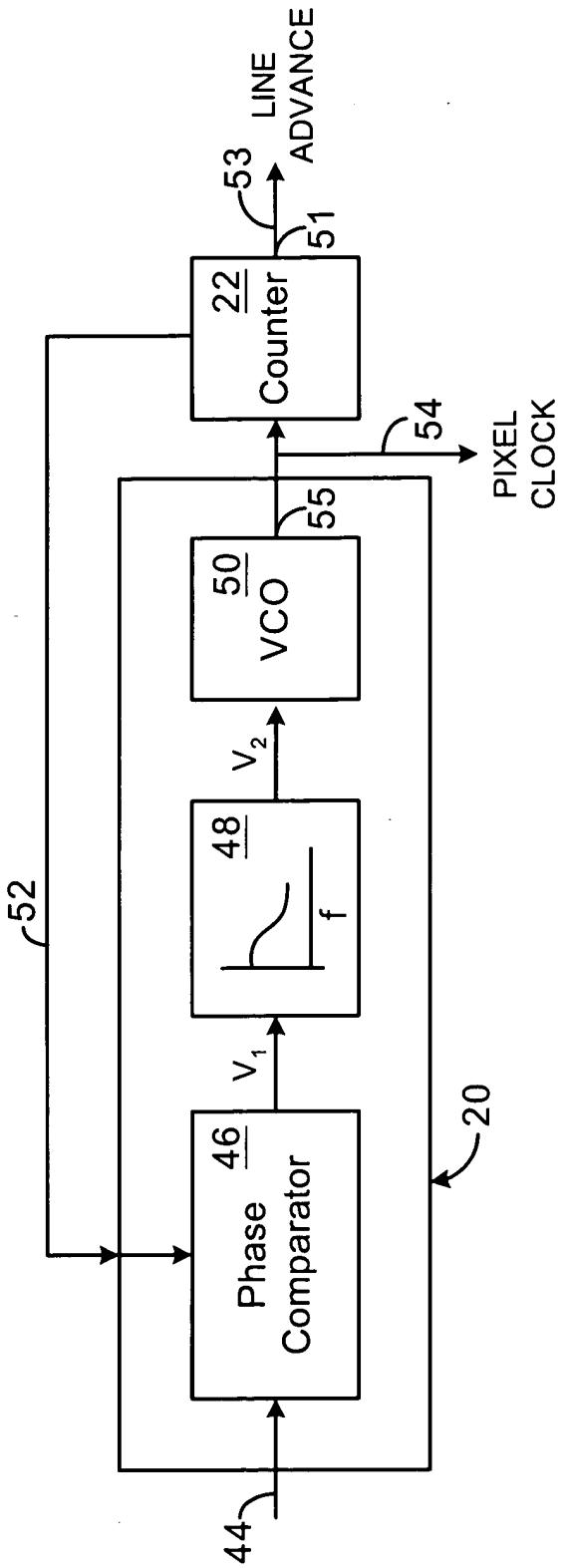

FIG. 6

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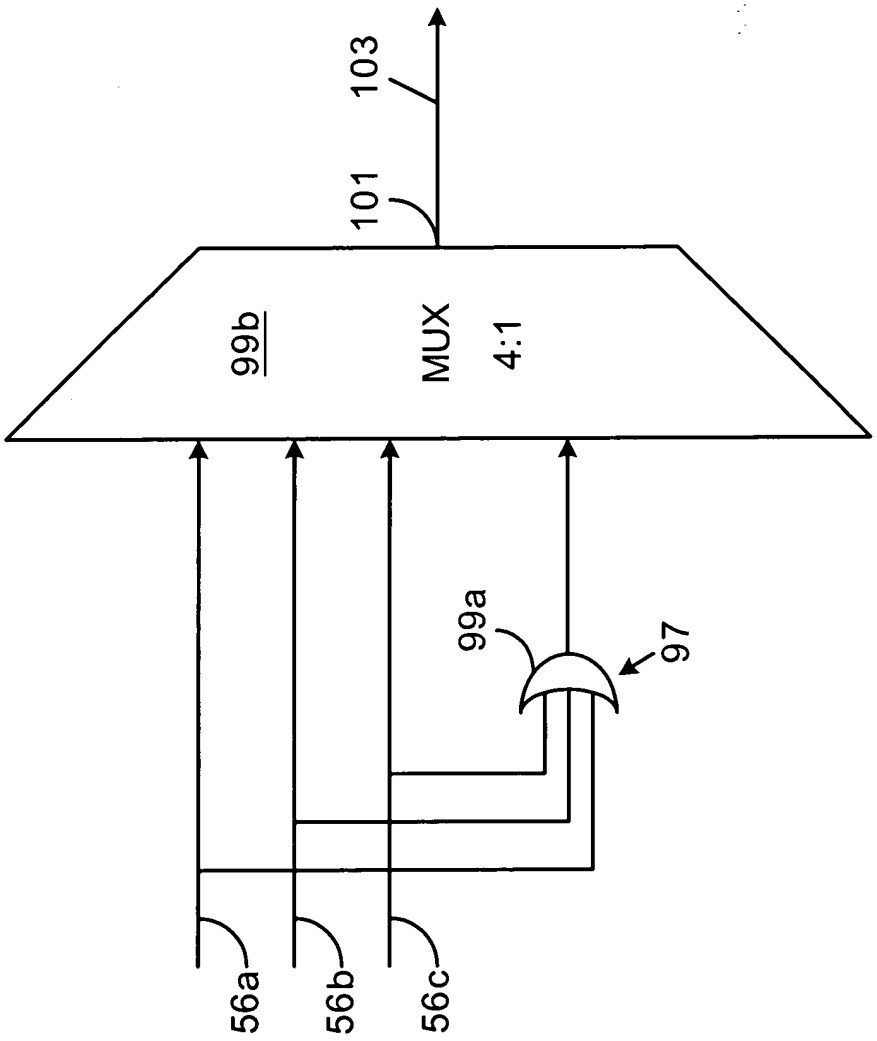

FIG. 7

PATENTE TRADEMARK OFFICE JUL 11 2003 JC104

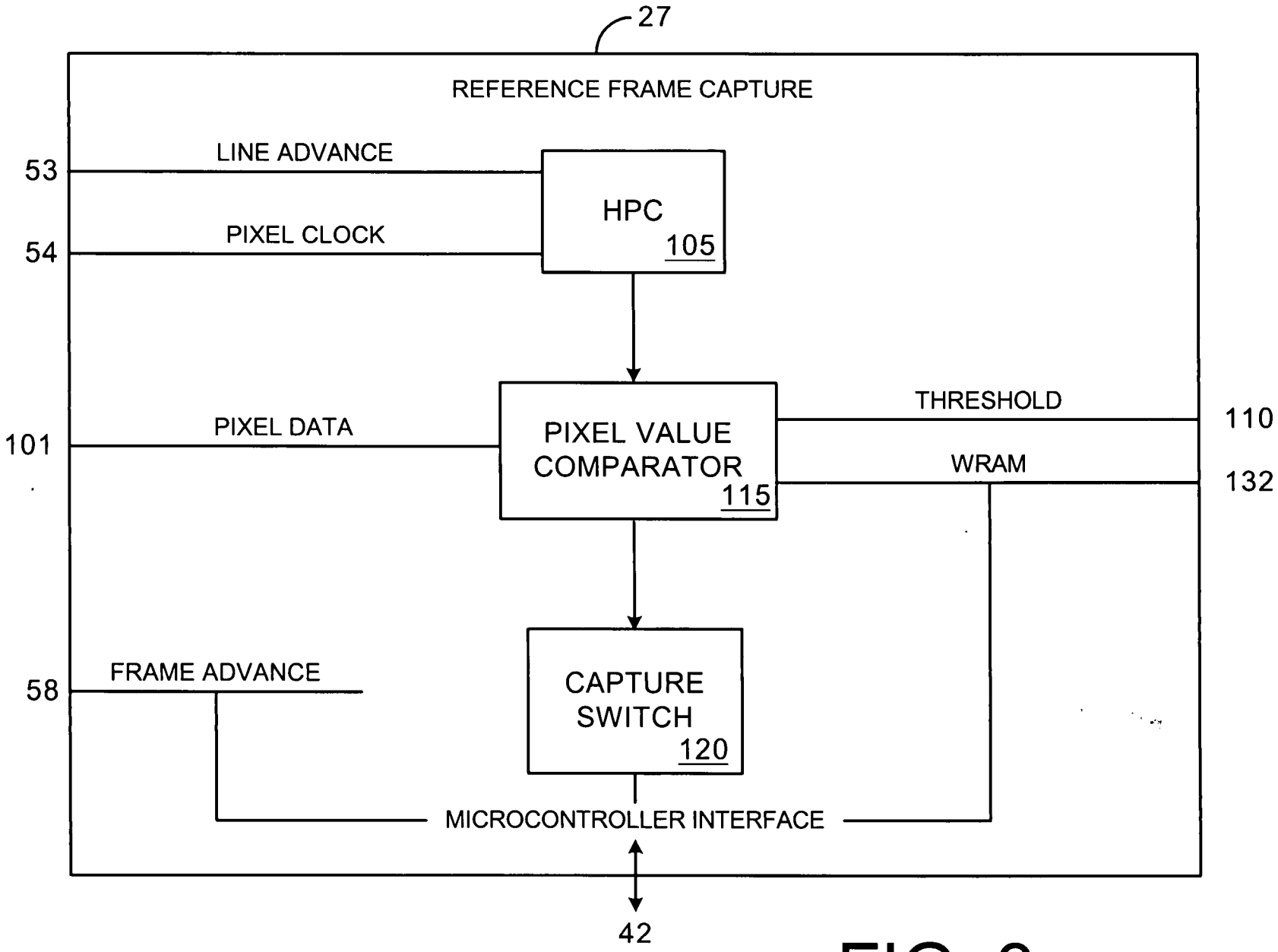

FIG. 8

OIPE JC184
JUL 11 2003
PATENT & TRADEMARK OFFICE

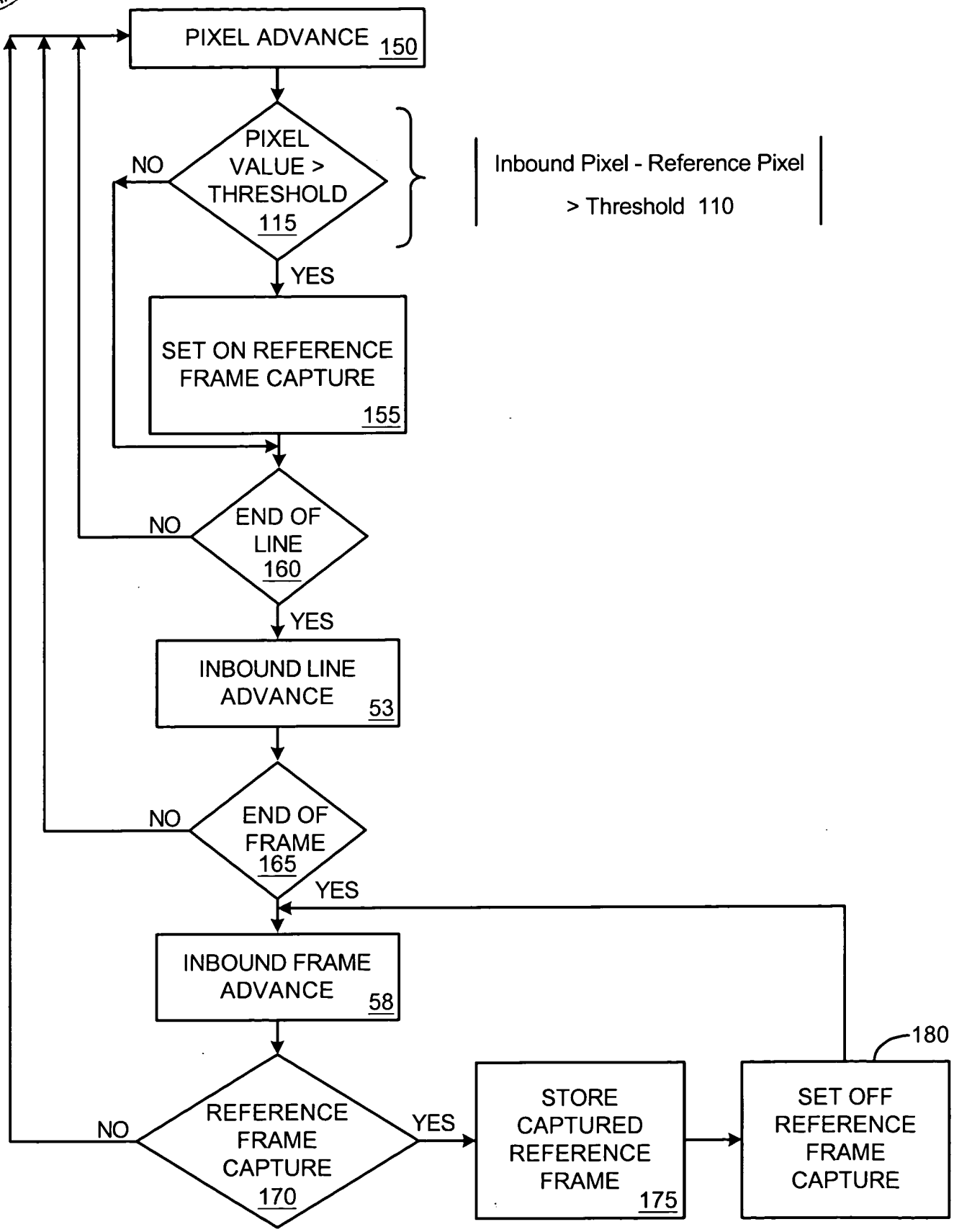

FIG. 9

PATENT & TRADEMARK OFFICE JUL 11 2003 USPTO

58 Frame Advance	55 Line Advance	HPC (Horizontal Position Counter) 105 1	2	3	4	5	6	7	· · · · · · · · ·		115 Pixel Value Comparison	Action
1	Line 1	0	0	FF	0	0	0	0	FF	0	0	—	Store Reference Frame 175
	2	1	2	FC	1	0	1	0	FE	1	2		
	3	0	2	FE	0	1	2	0	FF	3	2		
	4	3	4	FC	0	1	[2]	0	FC	3	4		
	5	3	4	FC	2	3	FF	[3]	FC	0	2		
2	Line 1	0	0	FC	0	0	0	0	FC	0	0	[2 - 3] = 1 [3 - 3] = 0	No Change 176
	2	1	2	FD	2	0	2	0	FB	1	3		
	3	0	2	FF	0	1	3	0	FD	3	2		
	4	3	4	FC	0	2	[3]	0	FB	3	4		
	5	3	4	FB	2	3	FF	[3]	FC	0	2		
. . . . .	. . . . .												No Change 176
3232	Line 1	3	3	FC	0	0	0	0	FC	0	0	[3 - FF]=FC [3 - FF]=FC	Set on reference frame capture 115
	2	1	3	FD	2	0	2	0	FB	1	3		
	3	0	2	FC	0	3	3	0	FC	3	1		
	4	3	4	FC	0	1	[FF]	0	FB	3	4		
	5	3	4	FB	2	3	FF	[FF]	FC	0	2		
3233	Line 1	5	5	FF	2	3	2	3	FF	1	3	—	Store Reference Frame 175

FIG. 10

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SC184

INBOUND PIXEL 201	
R	245
G	200
B	87

REFERENCE (STORED) PIXEL 202	
R	241
G	204
B	200

				> 5
203	R	245 - 241	= 4 →	False
204	G	200 - 204	= 4 →	False
205	B	87 - 200	= 113 →	True

FIG. 11